



United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/749,819	12/28/2000	Hiroaki Fukuda	201392US2	5195
22850	7590 02/07/2006		EXAMINER	
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.			TUCKER, WESLEY J	
ALEXANDRIA, VA 22314			ART UNIT	PAPER NUMBER
	•		2623	
			DATE MAILED: 02/07/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	-	
09/749,819	FUKUDA ET AL.	FUKUDA ET AL.	
Examiner	Art Unit		

	Wes Tucker	2623				
The MAILING DATE of this communication appe	ars on the cover sheet with the c	orrespondence add	ress			
THE REPLY FILED <u>23 January 2006</u> FAILS TO PLACE THIS A	APPLICATION IN CONDITION FO	R ALLOWANCE.				
1. The reply was filed after a final rejection, but prior to or o this application, applicant must timely file one of the follop places the application in condition for allowance; (2) a No (3) a Request for Continued Examination (RCE) in comp following time periods:	owing replies: (1) an amendment, a otice of Appeal (with appeal fee) in	iffidavit, or other evide compliance with 37 (ence, which CFR 41.31; or			
a) The period for reply expires 3 months from the mailing date of	a) The period for reply expires <u>3</u> months from the mailing date of the final rejection.					
b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO						
MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).					
Extensions of time may be obtained under 37 CFR 1.136(a). The date on been filed is the date for purposes of determining the period of extension a CFR 1.17(a) is calculated from: (1) the expiration date of the shortened states above, if checked. Any reply received by the Office later than three monther armed patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL	nd the corresponding amount of the fee. atutory period for reply originally set in the	The appropriate extension final Office action; or (2)	on fee under 37 as set forth in (b)			
 The Notice of Appeal was filed on A brief in com- of filing the Notice of Appeal (37 CFR 41.37(a)), or any e Since a Notice of Appeal has been filed, any reply must be 	xtension thereof (37 CFR 41.37(e)), to avoid dismissal o	of the appeal.			
AMENDMENTS						
 The proposed amendment(s) filed after a final rejection, (a) They raise new issues that would require further co (b) They raise the issue of new matter (see NOTE below 	nsideration and/or search (see NO		because			
(c) They are not deemed to place the application in be	•	educing or simplifying	the issues for			
appeal; and/or		.tkdt				
(d) They present additional claims without canceling a	, -	ejected claims.				
NOTE: (See 37 CFR 1.116 and 41.33(a)).		ampliant Amandmani	(DTOL 224)			
4. The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324). 5. Applicant's reply has overcome the following rejection(s):						
 Newly proposed or amended claim(s) would be a the non-allowable claim(s). 	· ———	, timely filed amendn	nent canceling			
7. For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is pro The status of the claim(s) is (or will be) as follows:		ill be entered and an	explanation of			
Claim(s) allowed:						
Claim(s) objected to:						
Claim(s) rejected: Claim(s) withdrawn from consideration:						
AFFIDAVIT OR OTHER EVIDENCE						
B. The affidavit or other evidence filed after a final action, b because applicant failed to provide a showing of good ar and was not earlier presented. See 37 CFR 1.116(e).						
9. The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to a showing a good and sufficient reasons why it is necessar	overcome <u>all</u> rejections under appe	eal and/or appeilant fa	ils to provide a			
	•	, , ,	•			
IO. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached. REQUEST FOR RECONSIDERATION/OTHER						
 The request for reconsideration has been considered by See Continuation Sheet. 	,	\sim	inge because:			
12. Note the attached Information Disclosure Statement(s).13. Other:	(PTO/SB/08 or PTO-1449) Paper	No(s).				
•						
		∠ VIKKRAM	BALI			

U.S. Patent and Trademark Office PTOL-303 (Rev. 7-05) Continuation of 11. does NOT place the application in condition for allowance because: Applicant presents for the first time arguments with regard to the arrangement of the claimed "aritmetic processing unit" and a "programmable arithmetic processing section" to be included within the unit. It should be noted that this complaint about the cited prior art was not argued until now.

Let it now be understood that an:

"arithmetic processing unit configured to process image data relating to the image data received by the sensor board unit to provide processed image data representing a reproduction of said original document, said aritmetic unit including," reads, in its most specific form, on the invention of Kan. An arithmetic processing unit is nothing more than a COMPUTER or processing configuration such as the one shown in Kan in Figs. 1 and 2. It should be clear that Kan discloses his operations to take place ina computer.

The aritmetic processing unit cited in Kan of Fig. 2, element 108 was cited to show explicit aritmetic processing, but it must also be clearly appreciated that the arithmetic processing unit cited in the first part of claim 1 is exactly the same type of processor or system of processors as disclosed in the invention of Kan. There is no box depicting "an aritmetic processing unit ... including" in the Figures of the presently claimed invention. It is only cited in the claims in order to place it in the exceedingly well known area of hardware used for image processing. The Applicant must realize the scope of the invention and that the reference of Kan is disclosing a system in the same very well known area of hardware used for image processing.

The subsequently claimed:

"a programmable arithmetic processing section of SIMD type configured to provide simultaneous processing of plural image data portions" such as the that cited in element 50 of Fig. 1 of Kan is clearly contained in the arithmetic processing unit that is depicted in the entire units clearly shown in Fig. 1 or Fig. 2.

It should be exceedingly clear to the Applicant with any reading at all of the Kan reference that what Kan discloses is an aritmetic processing unit that indeed INCLUDES both SIMD and MIMD processing sections (See the Abstract or Title).

There can be no mistake that Kan discloses this explicitly in the title of the invention. A "parrallel data processing system" is an "aritmetic processing unit" and Kan discloses INCLUDING both SIMD and MIMD sections.

The rejection previously presented is accordingly maintained and remains FINAL.